

Abstract

Provided is a gas discharge display panel that exhibits a favorable display performance by maintaining a wall charge retaining power, controlling discharge delay within a range adequate for optimal image display, and reducing the discharge starting voltage at comparatively low cost. Also provided is a PDP that exhibits more reliability with enhanced display quality by further improving the secondary electron emission factor γ compared to conventional cases and lowering the discharge starting voltage to widen the driving margin. In addition, provided is a manufacturing method of a gas discharge display panel, by which the manufacturing cost lowers by reduction of the exhaustion time in the sealing exhaustion process, and by which the driving circuit cost is reduced. In the present invention, the protective layer contains, with respect to a MgO content of the protective layer, Si in a range of 20 mass ppm to 5000 mass ppm inclusive and H in a range of 300 mass ppm to 10000 mass ppm inclusive.